IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No
Priority Filing Date February 7, 2003
Inventor
Assignee Micron Technology, Inc.
Priority Group Art Unit
Priority Examiner Rehana Perveen
Attorney's Docket No MI22-2420
Title: Computer Including Optical Interconnect, Memory Unit, and Method of Assembling
a Computer

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, copending application of the above-identified application. The above-identified application is a continuation of co-pending Application Serial No. 10/359,778, filed February 7, 2003, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). Applicant submits copies of the articles for review.

Citation of these references is respectfully requested.

Respectfully submitted,

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Date: October 17, 2003

Deepak Malhotra Reg. No. 33,560

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EV 318284592

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. MI22-2420 SERIAL NO. Filed Herewith Form PTO-1449 APPLICANT WARREN M. FARNWORTH ET AL LIST OF ART CITED BY APPLICANT (Use several sheets if necessary) FILING DATE GROUP U.S. PATENT DOCUMENTS Subclass Document Date Class Filing Date *Examiner Name Initial Number If Appropriate 5,013,247 5/07/91 WATSON 5,237,609 8/17/93 KIMURA ΑB 5,347,384 9/13/94 McREYNOLDS ET AL. AD 5,841,628 11/24/98 ALPERT ET AL. ΑE 6,249,621 B1 6/19/01 SARGENT, IV., ET AL. 6,273,759 B1 PERINO ET AL. AF 8/14/01 6,052,498 4/18/00 PANICCIA AG ΑH 5,513,021 4/30/96 KANESHIRO ET AL. 5,479,539 12/26/95 GOLDSMITH ET AL. 6,157,966 12/5/00 MONTGOMERY ET AL. $\mathbf{A}\mathbf{K}$ 6,202,110 B1 3/13/01 COTEUS ET AL AL FOREIGN PATENT DOCUMENTS Document Country Subctass Translation Number Yes No AM AN ΑO ΑP AQ OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR GUILFOYLE, Peter S., et al., "Free-Space Interconnects for High-Performance Optoelectronic Switching", IEEE, pp. 69-75 (Feb. 1998) AT

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

APPLICANT WARREN M. FARNWORTH ET AL.

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APPLICANT
WARREN M. FARNWORTH E

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